EEE4120F 2023 Quiz0

This is a short quiz just to get a sense of how aware you already are of some HPES terms and topics.

*Required

| 1. | What do you think is the difference between a High Performance Embedded System (HPES) and a regular Embedded System (ES) . Think for a moment and then give a brief written response. [5min] | * |
|----|--|---|
| 2. | So you might or might not have heard of (or remember) what Verilog is. Which point below seems most correct? | * |
| | Mark only one oval. | |
| | It is a means to check logical expressions | |
| | It is a general-purpose programming language | |
| | It is used in high performance computing to run threads on a processor | |
| | It is a coding language for hardware description | |
| | None of these, engineering has nothing to do with 'verilogs' | |
| | | |
| 3. | What is AMD Xilinx Vivado? | |
| | Mark only one oval. | |
| | No idea - I would probably delete it from my PC if I found it there | |
| | It is a software tool for analyzing Inter-Species Expressions (like the face your dog makes when he sees your neighbor's cat and then barks like mad). | |
| | It is an application for converting C code into something BASIC | |
| | It is an application used to develop HDL code and programme FPGAs | |

| 4. | Spatial computing is what? Briefly explain it |
|----|---|
| 5. | Explain the difference between composition and aggregation in UML |
| 6. | Is there any difference between a thread and a process? Think how you would explain why this is. Mark only one oval. Yes No |
| 7. | Which of these is Amdahl's law used to judge for computation? Mark only one oval. Cost between two implementions Speed between two implementations |

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