

*This quiz is for marks!***PLEASE ANSWER ON A SEPARATE PAGE****MAKE SURE YOUR STUDENT NUMBER IS ON YOUR ANSWER PAGE(s)****TOTAL NUMBER OF QUESTIONS : FIVE**

time(min):

#	Question	Marks
Q1	<p>Timing is an important part of establishing whether one solution is better than another. For example, prac 1 involved implementing a median filter and using gettimeofday to measure the time it took. Answer the following:</p> <p>(a) What exactly is meant by the term 'wall clock time' in the context of benchmarking computation? [2 mark]</p> <p>(b) Present a shot counter argument to the premise: "wall clock time should be measured very accurately, +/- 1us if not better". [2 marks]</p> <p>(c) Briefly describe a recommended practice for timing programs to establish reliable time results (mention at least three methods; 1 mark for quality of answer). [4 marks]</p>	8
	<p>This algorithm relates to both Q2 and Q3.</p> <pre> Image pic = loadpicture("bigdaddy.jpg"); // load an existing 24-bit image Image out = new Image(pic.width,pic.height,8); // create a blank 8-bit canvas out.graymap(); // colour map ranges from RGB(0,0,0) for colour 0 to RGB(255,255,255) int q = split(); // the above line divides the program into two copies running as separate threads and // returns: q=0 for thread 1 (original program), q=1 for thread 2 (the new process) for (int y=q; y<p.height; y+=2) { for (int x=0; x<p.width; x++) { // do scaled RGB -> gray conversion unsigned intensity = (unsigned)((float) 0.299 * pic.px(x,y).r + 0.587 * pic.px(x,y).g + 0.114 * pic.px(x,y).b); // save back into new image out.px(x,y) = intensity; } } if (q==0) out.save("daddy2.gif"); </pre>	
Q2	<p>Answer the following questions in relation to the algorithm above:</p> <p>(a) In a sentence describe what does the above algorithm does? [2 marks]</p> <p>(b) Is this actually a parallel solution? Or do both threads do the same thing, meaning that you'd get exactly the same result if you removed the int q=split(); line in the code (and replaced it with int q=0;). Motivate your answer referring to the code if needed. [4 marks]</p> <p>(c) Choose the letter A-E to specify which type of method has been used to divide up the memory to work on it in parallel in the algorithm. [2 marks]</p> <p>A. Contiguous B. Partitioned C. Interleved D. Interlaced E. Haphazard</p>	8
Q3	<p>Answer the following questions in relation to the algorithm above:</p> <p>(a) The above algorithm is a 2-thread solution (the split() function causes the spawning off of a new thread), Briefly describe how you could make this into a 4-thread solution. You can add a code snippet or diagram to help your explanation. [7 marks]</p> <p>(b) The speed-up was a factor of 1.2 when moving from the 1-thread to the 2-thread solution shown above. Assuming that you are running on a 4-core machine, discuss and motivate what speed-up you would expect when moving from this 2-thread solution to a 4-thread solution. [3 marks]</p>	10

Q4	<p>This question relates to textbook CH13. Refer to formulae below if needed.</p> <p>Consider the following system built around a hypothetical Power Vector Processor (PVP) microprocessor chip. The PVP can do 64 simultaneous register arithmetic operations each clock cycle, and can turn off any number of its vector operations (e.g., running from 1 to a maximum of 64 simultaneous operations) per clock. When each vector operation is active it draws current, but when turned off draws zero current (its so little current you can assume it is 0 anyway). The main application that the PVP run involves the following arithmetic operations in the <i>main loop</i> :</p> <ul style="list-style-type: none"> 10 000 x sequential (single-operation) operations 10 000 x 32 simultaneous vector operations 20 000 x 64 simultaneous vector operations <p>The VPV operates at a 100MHz clock (note each instruction compeltes in just 1 clock cycle).</p> <p>(a) What is the relevance of the Required Computation Rate (RCR) metric? Explain the reason for determining this number before completing the design of a system. [2 mark]</p> <p>(a) Assuming system just continues running the main loop of operations as described above, what is the <i>peak computation rate</i> for that part of the system? [3 marks]</p> <p>(b) Consider a certain program for which the PVP executes 40 000 sequential instructions (i.e. ops with one vector core turned on) and then ten repeats of the main loop, and then exits. How long would this program take to run? [3 marks] Considering that there are only arithmetic opetations in the main loop, what is the <i>sustained computation rate</i> for running this program? [3 marks]</p> <p>(c) If the PVP processor draw on avarage 50W while running the main loop, what can you say about the average power efficiency when the system is running the loop? [3 marks]</p>	11
Q5	<p>(a) Briefly define cloud computing. [3 marks]</p> <p>(b) What is the relation between the hypervisor and the operating system(s) used in a cloud computing system? [2 marks]</p> <p>(c) What is meant by a course-grained problem? Give one example of such a problem or application. [3 marks]</p>	8
Q6	<p>** BONUS MARK QUESTION: **</p>	[2]
	<p>In Professor Olukotun talk on 23 March, he spoke about which of the following strategies that the Stanford Pervasive Parallism Lab (PPL) is focusing on as a possible solution to programming parallel systems...? (choose a letter below)</p>	
	<p>A. C++ and Java automatic parallelism B. Partitioning using MPI C. Domain specific lanaguages D. CPU + GPU clusters E. lots of simple processors</p>	
TOTAL :		45

Appendix -- useful formulae

Required Computation Rate (RCR) = num operations to be executed / time available for computation

Peak Computation Rate = (num arithmetic processor operations per clock cycle) x (maximum clock rate)

Sustained Computation Rate = num arithmetic operations executed by program / time program takes to run

Achievable Efficiency = Sustained Computation Rate / Peak Computation Rate

Power efficiency = computation rate / power consumed

Communication-To-Computation Ratio = time spent calculating / time spent communicating

Power Consumption of CMOS device: $P = CfV^2$

where: C = Gate Capacitance f = Clock Frequency V = Supply Voltage